



PATENT
CASE NAME/No.: SP01-319

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Robert A. Bourdreau, et al.

Serial No: 10/016,113

Filing Date: 11/2/2001

Title: SILICON WAFERBOARD

Art Group Unit: 2874

Examiner: Juliana K. Kang

LETTER TO OFFICIAL DRAFTSPERSON

Commissioner for Patents
Alexandria, VA 22313-1450

LETTER TO OFFICIAL DRAFTSPERSON

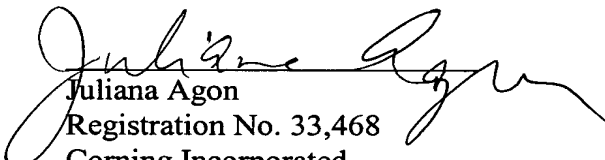
Enclosed for your review and approval are 2 (two) sheets of formal drawings conforming to Figures 1 - 6 in the above entitled application, amended or corrected as discussed with or required by the Examiner in the corresponding papers within the file history of this application.

Please contact the undersigned as soon as possible in the event these drawing Figures do not conform to the Rules or other requirements of the Office, if any sheet appears to be missing or is damaged in transit or handling, or if the Official Draftsman or Examiner have further suggested modifications or revisions to the drawings for consideration by Applicants.

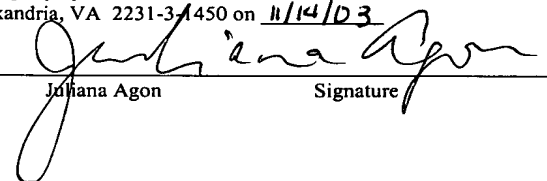
Respectfully submitted,

CORNING INCORPORATED

Date: November 14, 2003


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CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8: I hereby certify that this paper and any papers referred to herein are being deposited with the U.S. Postal Service, as first class mail, postage prepaid, addressed to the Commissioner of Patents, Alexandria, VA 22313-1450 on 11/14/03.


Juliana Agon
Signature